

CZT

SPECIFICATION FOR APPROVAL

CUSTOMER _____

CUSTOMER'S PART NO. _____ REV. _____

DESCRIPTION RJ45 ICM (1000Base-T 2Port)

CZT'S PART NO. JDG210720003 REV. B

ISSUE NO. _____

ISSUE DATE JUN.25,2013

☐ APPROVED	☐ REJECTED:
☐ APPROVED UNDER THE CONDITIONS:	

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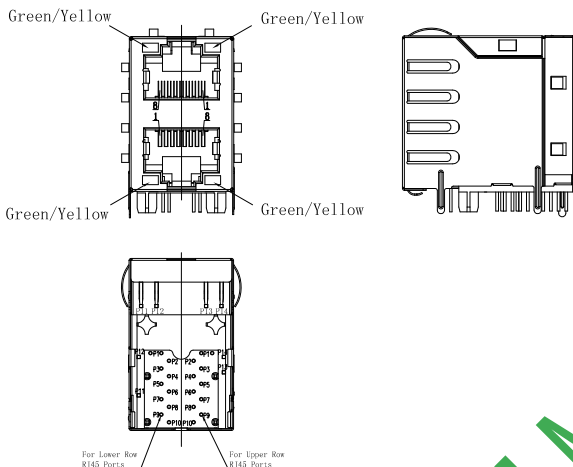
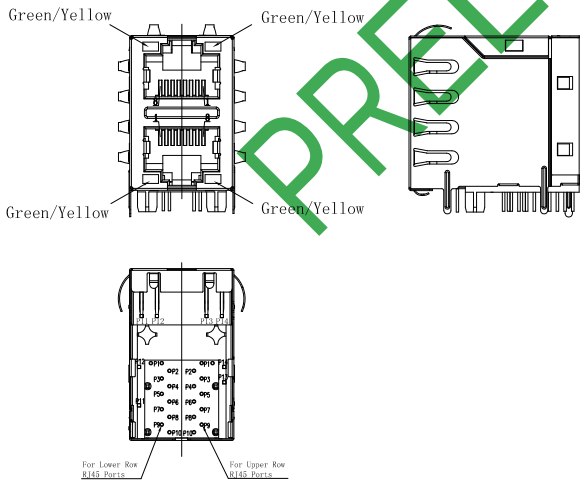
Note:

1. This specification is the proprietary of Yihua Communicated Connector Co., Ltd. and is not allowed to be revealed to third party without any written approval.
2. This specification is valid within 1 year before approval, and for any updated specification information required, please contact with CZT's sales window before order releases.

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2. Revision Change Record

Customer Part No.: CZT Part No.: JDG210720003 Part Name: RJ45 ICM(1000Base-T 2Port)					Rev.: B
					
Rev	Description	Drawn	Designed	Approved	Issue Date
A	Specification issued	ChenLingjuan	SunWenqun	Topup.Liu	FEB.25 2013
B	Change From: 4-1. Mechanical Dimension: 	ChenLingjuan	SunWenqun	Topup.Liu	JUN.25 2013
	To: 4-1. Mechanical Dimension: 				

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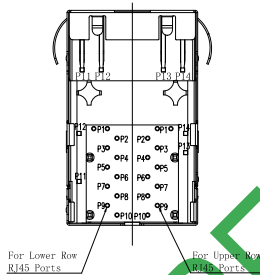
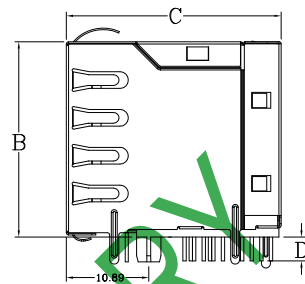
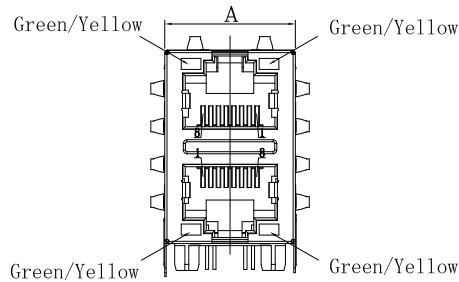
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4. Mechanical Dimension and Notes: 4-1. Mechanical Dimension:



UNIT : mm / inch
A = 17.21 / 0.678
B = 25.79 / 1.006
C = 28.40 / 1.116
D = 3.20 / 0.126

Unless otherwise specified, all dimensions
tolerances is ± 0.254 / 0.010.

4-2. Notes:

- CONNECTOR MATERIAL:
HOUSING: THERMOPLASTIC UL94 V-0
SHIELD: Brass
SHIELD PLATING: NICKEL
CONTACT: PHOSPHOR BRONZE
CONTACT PLATING: GOLD, 30 MICRO-INCHES MIN IN CONTACT AREA
- PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED SEE ELECTRICAL DRAWING FOR OMITTED PINS
- RJ45 CAVITIES CONFORM TO FCC RULES AND REGULATION PART 68.
- THE PART IS RECOMMENDED FOR WAVE SOLDERING PROCESS PEAK SOLDERING TEMPERATURE IS 260°C MAX, 10 SECS MAX
- OPERATING TEMPERATURE $T=0^{\circ}\text{C}$ TO $+70^{\circ}\text{C}$.
- STORAGE TEMPERATURE $T=-40^{\circ}\text{C}$ TO $+85^{\circ}\text{C}$.

LED SPECIFICATION				
STANDARD LED	WAVELENGTH	Forward Current	Forward V(max)	TYP
GREEN	565nm	20 mA	2.4V	2.2V
YELLOW	590nm	20 mA	2.5V	2.1V

Drawn by

ChenLingjuan

Designed by

SunWenqun

Approved by

Topup.Liu

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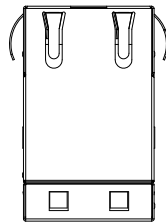
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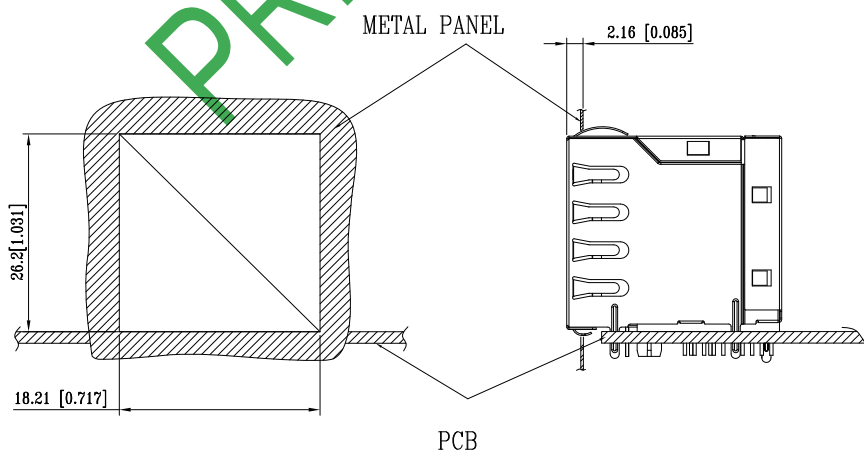


5. Label and Recommended PWB Layout and Suggested Panel Opening:
5-1. Label:

Plug In Side



- 5-2. Suggested Panel Opening:



UNIT: mm / inch
Tolerances: ± 0.10 / 0.004

REFERENCE ONLY

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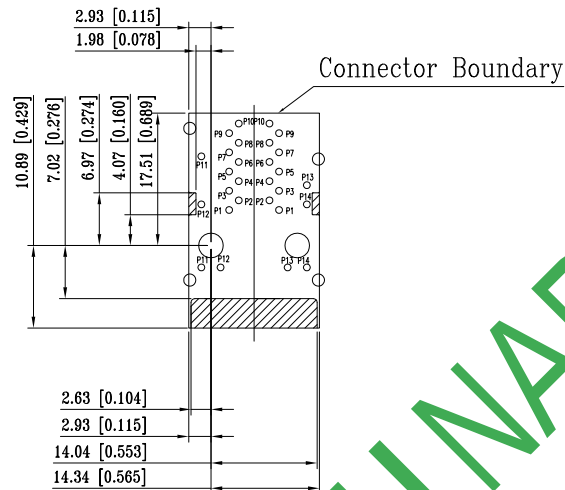
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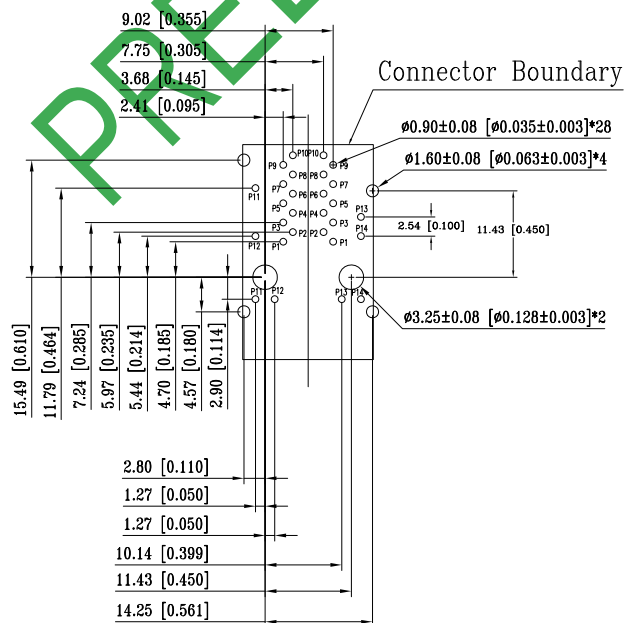
5-3. Keep-Out Area (Component Side View):



Plug-in side

UNIT: mm / inch
Tolerances: ± 0.10 / 0.004

5-4. Recommended PWB Layout (Component Side View):



Plug-in side

UNIT: mm / inch
Tolerances: ± 0.10 / 0.004

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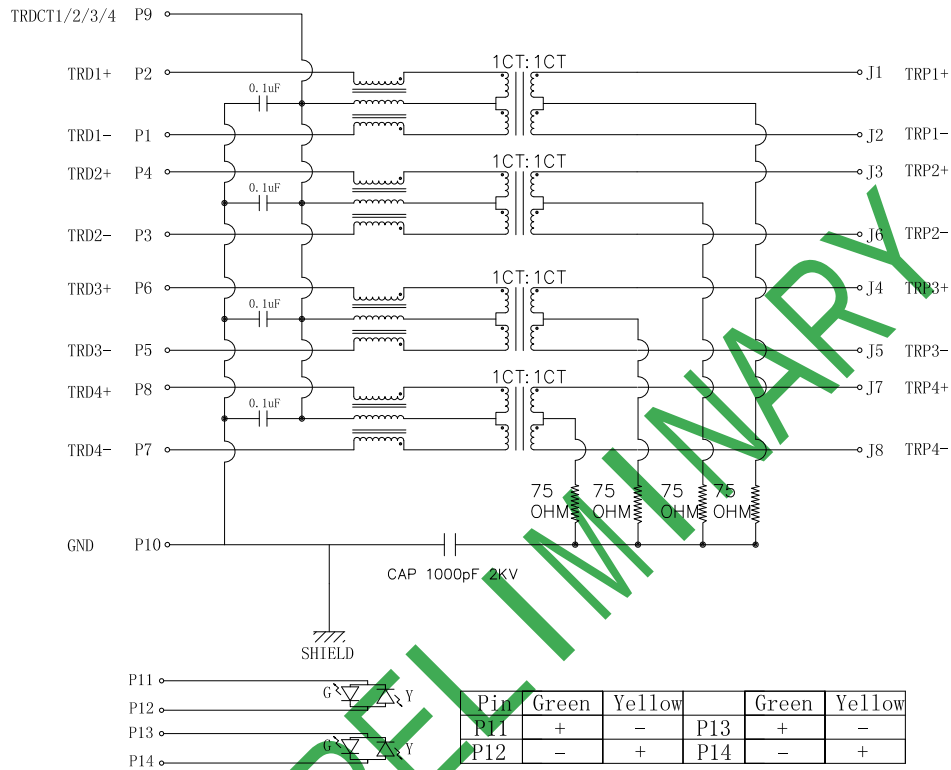
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6. Circuit Schematic and Electronical Specifications:

6-1. Circuit Schematic:



6-2. Electronical Specifications:

- 1.0 Turn Ratio @100KHz: $(P2 \sim P1):(J1 \sim J2) = 1:1 \pm 5\%$
 $(P4 \sim P3):(J3 \sim J6) = 1:1 \pm 5\%$
 $(P6 \sim P5):(J4 \sim J5) = 1:1 \pm 5\%$
 $(P8 \sim P7):(J7 \sim J8) = 1:1 \pm 5\%$
- 2.0 Primary Inductance: 350uH MIN @100KHZ, 0.1V 8mA DC BIAS
- 3.0 DC Resistance: 1.2 OHMS MAX
- 4.0 Insertion Loss: 1-100MHz -1.0dB MIN
100-125MHz -1.2dB MIN
- 5.0 Return Loss: 1-40MHz -16dB MAX
40-60MHz -12dB MAX
60-80MHz -10dB MAX
80-100MHz -8dB MAX
- 6.0 Cross Talk: 1-100MHz -30dB MAX
- 7.0 COMMON TO COMMON MODE ATTENUATION: 1-100MHz -30dB MIN
- 8.0 Isolation: PHY Side to Line Side: 2250VDC